

LSI LOGIC

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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035

Date February 24, 2006

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To: US PTO

Mail Stop Status Request

TC 2858

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191
No.

Fax No. (408) 433-7460

REMARKS:

☐ Urgent☐ For your review☒ Reply ASAP☐ Please commentApplication Number: 10/719,878
Filing date: November 21, 2003
First named inventor: Jeff Brown et al.
Attorney docket number: 03-0940 / 5201-27900

Transmitted herewith for filing via facsimile:

- Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

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Connie del Castillo

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FEB 24 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Jeff Brown et. al.

Serial No. :

10/719,878

Filed :

November 21, 2003

For :

Device And Method For Using A
Lessened Load To Measure Signal
Skew At The Output Of An
Integrated Circuit

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Group Art Unit :

2858

Examiner :

Atty Docket :

5201-27900 / 03-0940

STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

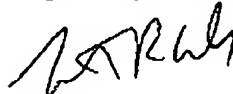
Sir:

Applicants request a status of the above referenced application. Please mail
the status notification to the address stated below.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
T+11 408-433-7191

Date: 23 FEB 06

Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

Reg. No. 36,771